



Product Type: ASC/3 Controller **Logic Processor – International Pedestrian Clearance**

Reference: AN2076
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The Peoples Republic of China and other countries around the world typically utilize a flashing Walk signal as the Pedestrian Clearance indication at a signalized intersection. This function can be implemented in an ASC/3 Controller by utilizing the Logic Processor.

The Flashing Walk function is timed normally during the Pedestrian Clearance interval. This yields the desired signal indication sequence of Don't Walk – Walk – Flashing Walk – Don't Walk.

For this example we will assume a typical 8-phase intersection in a Dual-Quad configuration with Pedestrian movements on the even numbered phases. The Logic Processor programming is as follows:

Step 1:

```
IF Pedestrian Clearance 2 is ON
AND Don't Walk 2 is ON
THEN Set Walk Phase 2 ON
     Set Don't Walk Phase 2 OFF
```

Step 2:

```
IF Pedestrian Clearance 4 is ON
AND Don't Walk 4 is ON
THEN Set Walk Phase 4 ON
     Set Don't Walk Phase 4 OFF
```

Step 3:

```
IF Pedestrian Clearance 6 is ON
AND Don't Walk 6 is ON
THEN Set Walk Phase 6 ON
     Set Don't Walk Phase 6 OFF
```

Step 4:

```
IF Pedestrian Clearance 8 is ON
AND Don't Walk 8 is ON
THEN Set Walk Phase 8 ON
     Set Don't Walk Phase 8 OFF
```

In the above example, the first step (#1) checks if the desired phase (2) is timing the Pedestrian Clearance interval (Pedestrian Clearance 2 is ON), AND checks if the Don't Walk output is ON (Don't Walk 2 is ON) (flashing Don't Walk). It then turns the phase 2 Walk output ON and the Don't Walk output OFF. The second through fourth steps repeat the process for phases 4, 6 and 8.

If the International Pedestrian function is to be implemented on a Time-of-Day basis it may be accomplished simply by enabling or disabling the appropriate Logic Processor Steps.

This Logic Processor programming is suitable for use in the "hidden area" (above step 100) with an EXT file since the normal controller timing is utilized and only the Walk and Don't Walk signal outputs are



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swapped during Pedestrian Clearance. For maximum flexibility, sixteen steps should be programmed, one for each phase. However, placing the programming in the upper numbered steps can obscure the programming from a casual observer while still allowing modification from the controller keyboard if necessary and selectively turning a phase on and off by TOD. When placing the programming in the upper steps be aware of the processing sequence restrictions for the Logic Processor. Refer to the base ASC/3 Logic Processor Programming Application Note for further details.